

Team Name: sdmay21-

Team Members:

Report Period: Oct 12-Oct 18

Summary of Progress in this Period

This period the scope of the project was clarified resulting in a push in progress in the correct direction. We are focusing the act of researching and the original implementation of the high frequency pulser and our deliverables will be an in depth analysis, simulation, and recommended improvements for this device. This week we were able to talk with a new person who is an expert on the topic and they gave us a direction to move with the project. They gave us detailed instruction on using the simulation tools and assigned us to experiment with a part of the final circuit design for a follow up meeting in the coming week. We have been working on creating these simulations and tinkering with them. In this time we have also complete assignments for the 491 class that helped us become better designers and team members. Furthermore, we also created V2 of the design document.

Pending Issues

The biggest pending issues are that, using Simulink through the VDI is currently not working for some of the team members. Another issue is involving time. There is a lot to do and not a lot of time left in the semester.

Plans for Upcoming Reporting Period

The major plans for this period are to continue working with our new contact on the simulation side of things until we have a complete understanding of the simulation tools and impact on the circuit of varying values of different components or adding components.
